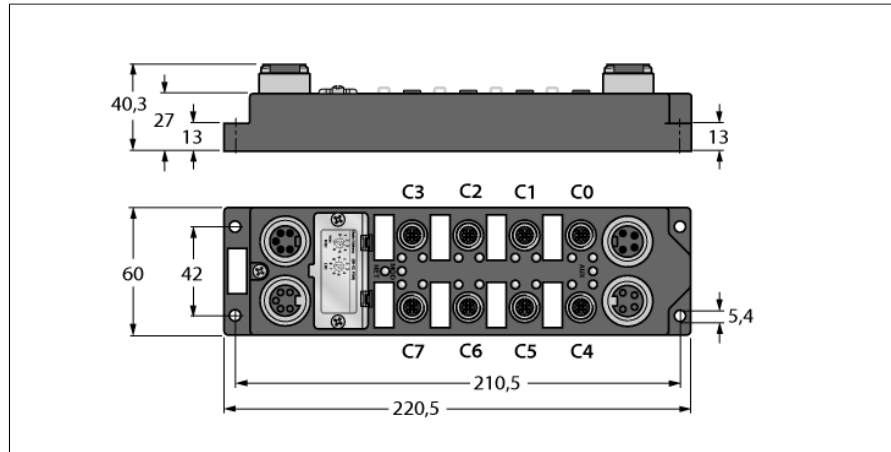


I/O Module for DeviceNet Fieldbus

16 configurable digital channels

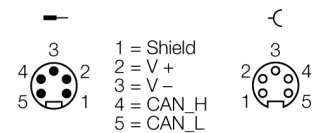
pnp inputs / outputs 0.5 A

FDNP-XSG16-TT

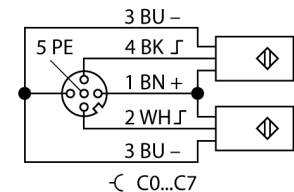


- 16 configurable digital channels
- Short-circuit monitoring
- Module-related diagnostics
- Two channel per connector
- Separate actuator power supply
- Fibre-glass reinforced PA6 housing
- Vibration and shock-resistant
- Encapsulated module electronics
- Metal connectors
- Degree of protection IP67

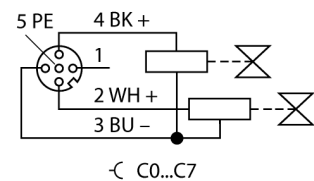
Fieldbus



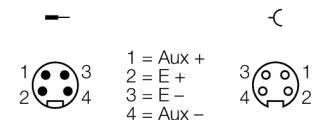
M12 × 1 Input



M12 × 1 Output



7/8" Power Supply



Type	FDNP-XSG16-TT
ID	F0129
Number of channels	16
Operating / load voltage	11...26 VDC
Operating current	< 75 mA
Voltage supply connection	2 × 7/8"
Inputs	
Number of channels	(16) 3-wire pnp sensors
Input voltage	13...26 VDC
Supply current	< 700 mA per module, short-circuit proof
Switching threshold	EN 61131-3 low max.: 1.5 mA / high min.: 2 mA
Input delay	2.5 ms
Switching frequency	≤ 100 Hz
Max. input current	7 mA
Electrical isolation	galvanic isolation against the bus
Outputs	
Number of channels	(16) DC actuators
Output voltage	24 VDC
Output current per channel	0.5 A, short-circuit proof
Load type	resistive, inductive, lamp load
Switching frequency	≤ 100 Hz
Simultaneity factor	1
Electrical isolation	galvanic isolation against the bus
Sensor supply	bus connection
Actuator power supply	separate (Aux)
Fieldbus transmission rate	125/250/500 kbps
Fieldbus addressing	0...63 (decimal) via coded rotary switches
Electrical isolation	to operating and load voltage

Dimensions (W x L x H)	60 x 220.5 x 27 mm
Housing material	fibre-glass reinforced Polyamide (PA6-GF30)
Mounting	4 mounting holes Ø 5,4 mm
Ambient temperature	-40...+70 °C
Protection class	IP67
MTTF	189 years acc. to SN 29500 (Ed. 99) 20 °C
Approvals	CE, UL, CSA, FM

Data in process image

C1P4: Male Connector 1, 4-pole

IGS: Wire-break/ short circuit - group signal

OGS: Short-circuit - group signal

		Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
Input	Byte 0	C3P2	C3P4	C2P2	C2P4	C1P2	C1P4	C0P2	C0P4
	Byte 1	C7P2	C7P4	C6P2	C6P4	C5P2	C5P4	C4P2	C4P4
	Byte 2	IGS	OGS	–	–	–	–	–	–
Output	Byte 0	C3P2	C3P4	C2P2	C2P4	C1P2	C1P4	C0P2	C0P4
	Byte 1	C7P2	C7P4	C6P2	C6P4	C5P2	C5P4	C4P2	C4P4